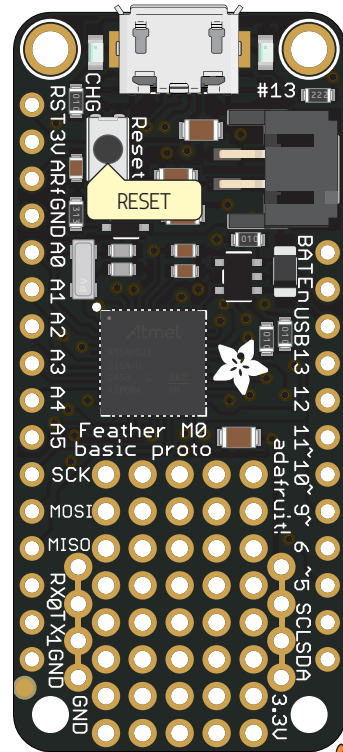
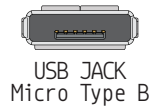


# feather

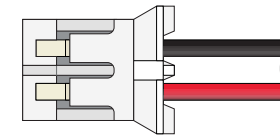
## M0 Basic

### PINOUT



Can't go higher than 3.3V

	RESET	4.0					
	3V3						
	AIN1	VREFA	EINT <sup>3</sup>	PA02	4		
				GND			
14	A0	AIN0	DAC	EINT <sup>2</sup>	PA02	3	
15	A1	AIN2	S <sup>4:0</sup>	EINT <sup>8</sup>	PB08	7	
16	A2	AIN3	S <sup>4:1</sup>	EINT <sup>9</sup>	PB09	8	
17	A3	AIN4	S <sup>0:0</sup>	VREFB	EINT <sup>4</sup>	PA04	9
18	A4	AIN5	S <sup>0:1</sup>	EINT <sup>5</sup>	PA05	10	
19	A5	AIN10	S <sup>5:0</sup>	EINT <sup>2</sup>	PB02	4.7	
24		SCK	S <sup>4:3</sup>	I2SCL	EINT <sup>11</sup>	PB11	20
23		MOSI	S <sup>4:2</sup>	I2SMC	EINT <sup>10</sup>	PB10	19
22		MISO	S <sup>2:0</sup>	I2C	PA12	21	
0		RX	S <sup>0:2:3</sup>	I2SF0	EINT <sup>11</sup>	PA11	16
1		TX	S <sup>0:2:2</sup>	I2SCK	EINT <sup>10</sup>	PA10	15
				GND			



Optional Lipoly Battery

VBAT  
 En Connect to ground to disable the 3.3V regulator  
 VBUS

- Power
- GND
- Physical PIN
- Port PIN
- Analog PIN
- Serial PIN
- PIN Function
- Interrupt PIN
- Control PIN
- IDE

PWM Pin  
 Port power group

The total current of each port power group **should not exceed** 65mA

**Absolute** MAX per pin 10mA, 7mA recommended

**Absolute** MAX 130mA for the entire package

VBUS Connected to 5V USB Port **Absolute** MAX 500mA

VBAT It's the positive voltage from to JST Batt jack

3V3 3V3 output from regulator **Absolute** MAX 400mA



<https://www.adafruit.com/product/2772>

